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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/674,790	10/01/2003	Kiyoshi Yoneda	492322013900	4157

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MORRISON & FOERSTER LLP  
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EXAMINER

XIAO, KE

ART UNIT PAPER NUMBER

2629

DATE MAILED: 05/01/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/674,790

Applicant(s)

YONEDA, KIYOSHI

Examiner

Ke Xiao

Art Unit

2629

— The MAILING DATE of this communication appears on the cover sheet with the correspondence address —

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 01 October 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-6 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-6 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 11/15/2005.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

**Claims 1-6** are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Yamazaki (US 2002/0021268).

Regarding independent **Claim 1**, Yamazaki teaches an electroluminescent display device (Yamazaki, Pg. 1 paragraphs [0001-0002]), comprising:

a plurality of pixels (Yamazaki, Figs. 1A-1B);

an electroluminescent element disposed in each of the pixels (Yamazaki, Fig. 10A element 1009);

a pixel selecting thin film transistor disposed in each of the pixels and selecting the corresponding pixel in response to a gate signal (Yamazaki, Fig. 10A element 1001); and

a driving thin film transistor disposed in each of the pixels and supplying an electric current to the corresponding electroluminescent element in response to a display signal supplied through the corresponding pixel selecting thin film transistor, the driving thin film transistor comprising a plurality of gates (Yamazaki, Fig. 10A elements 1004a and 1004b).

Regarding **Claim 2**, Yamazaki further teaches that the pixel selecting thin film transistor is configured to have only one gate (Yamazaki, Fig. 10A element 1001).

Regarding **Claim 3**, Yamazaki further teaches that the total number of gates of the pixel selecting thin film transistor is smaller than the total number of gates of the driving thin film transistor (Yamazaki, Fig. 10A elements 1001 1004a and 1004b).

Regarding **Claim 4**, Yamazaki further teaches that the driving thin film transistor comprises a plurality of transistors connected in parallel (Yamazaki, Fig. 10A elements 1004a and 1004b).

Regarding independent **Claim 5**, an electroluminescent display device (Yamazaki, Pg. 1 paragraphs [0001-0002]), comprising:

- a plurality of pixels (Yamazaki, Figs. 1A-1B);

- an electroluminescent element disposed in each of the pixels (Yamazaki, Fig. 15 element 1511);

- a pixel selecting thin film transistor disposed in each of the pixels and selecting the corresponding pixel in response to a gate signal (Yamazaki, Fig. 15 element 1506);

and

- a driving thin film transistor comprising a set of transistors connected in series, the driving thin film transistor being disposed in each of the pixels and supplying an electric current to the corresponding electroluminescent element in response to a display signal supplied through the corresponding pixel selecting thin film transistor (Yamazaki, Fig. 15 element 1508 left two transistors of the current mirror are connected in series).

Regarding **Claim 6**, Yamazaki further teaches that the driving thin film transistor further comprises an additional set of transistors connected in series, and the two sets of transistors connected in series are connected in parallel (Yamazaki, Fig. 15 element 1508 left two transistors are connected in parallel to the right two transistors).

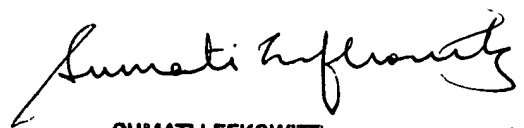
### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ke Xiao whose telephone number is (571) 272-7776. The examiner can be reached on Monday through Friday from 8:30AM to 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (571) 272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

April 25<sup>th</sup>, 2006 - kx -



SUMATI LEFKOWITZ  
SUPERVISORY PATENT EXAMINER